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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Application Number	10/626,264
Filing Date	July 23, 2003
First Named Inventor	David LEWIS
Art Unit	2826
Examiner Name	F. Abraham
Attorney Docket Number	306812005500

10/626.264

Complete If Known

U.S. PATENT DOCUMENTS						
Examiner	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant	
Initials*	No. Number-Kind Code <sup>2</sup> (if known) MM-C	MM-DD-YYYY	Applicant of Cited Document	Figures Appear		
1777	1	5,789,791	8/4/1998	Bergemont		
	2.	5,831,316	11/3/1998	Yu et al.		
	3.	6,197,671	3/6/2001	Bergemont		

1

FOREIGN PATENT DOCUMENTS						
Examiner	Cite	Foreign Patent Document	Publication Date  MM-DD-YYYY	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
initiats*	No.	Country Code <sup>a</sup> -Number <sup>a</sup> -Kind Code <sup>a</sup> (il known)		Applicant of Cited Document		

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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
FA	4.	Cetiner, B.A. (2002) "Global Modeling Approach for Pre-Matched Multifinger FET," Microwave and optical tech. letters, (32):174-178.		
	5.	Martin, Ken (2000). "Digital Integrated Circuit Design" Chapter 2 In Processing, Layout, and Related Issues. Oxford University Press, pp. 48-57.		
	6.	Rabaey, Jan M. et al. (2003). "Digital Integrated Circuits: A Design Perspective" Chapter 9 <i>In</i> Coping with Interconnect. C.G. Sodini ed., Prentice-Hall of India, pp. 456-457.		
	<b>/</b> 1.	Weste, Neil H. E. et al., (1994). "Principle of CMOS VLSI Design: A System Perspective" Chapter 4 In Circuit Characterization and Performance Estimation. P. S. Gordon ed., Addison Wesley Longman, pp. 186-188, 277-278.		

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